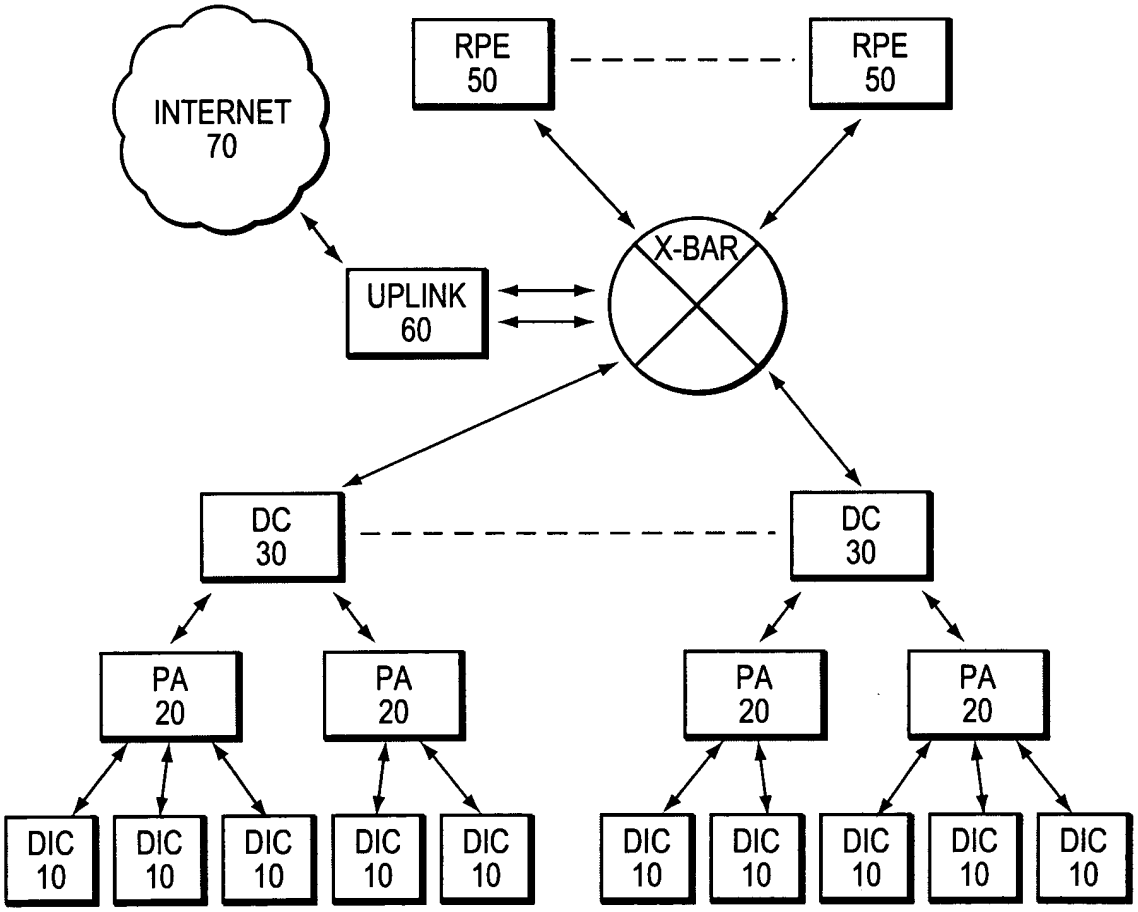
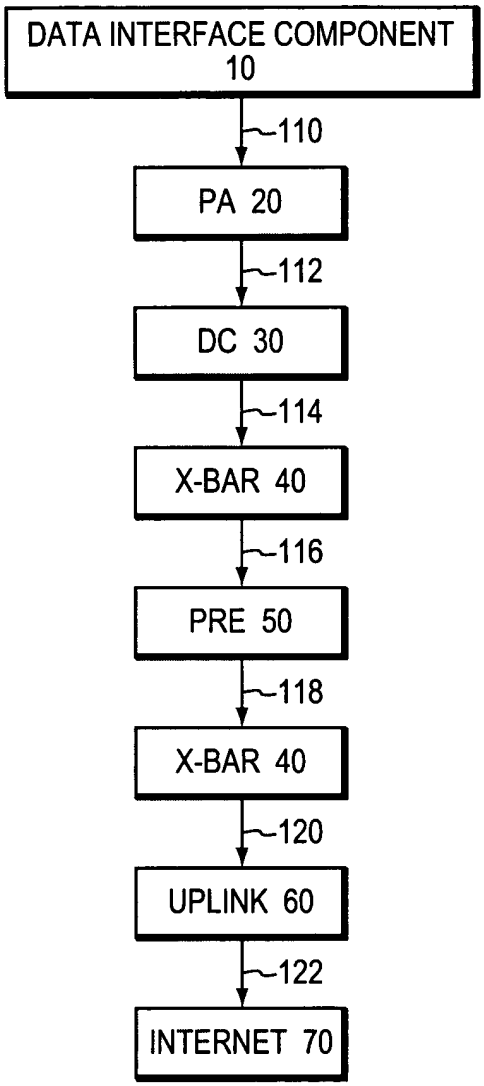
OIPE JC46
DEC 29 2004
PATENT & TRADEMARK OFFICE



ROUTER ARCHITECTURE

FIG. 1

OIPE JC46
DEC 2 9 2004
PATENT & TRADEMARK



SAMPLE FLOW DIAGRAM FOR A DATA PACKET

FIG. 2

OIPE
DEC 2 9 2004
PATENT & TRADEMARK OFFICE

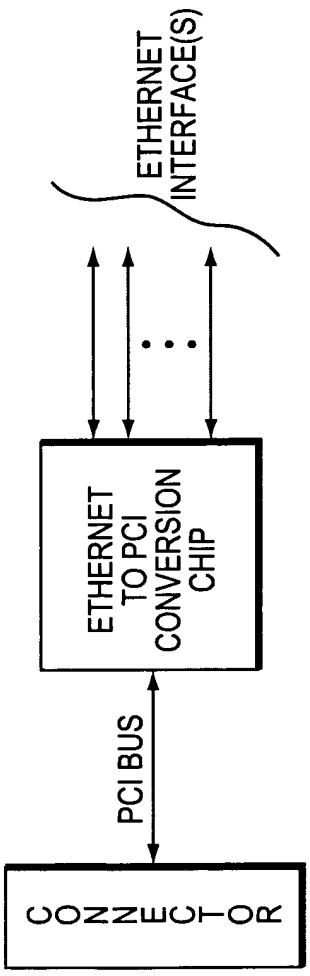


FIG. 3A

PORT ADAPTER DESIGN FOR AN ETHERNET INTERFACE

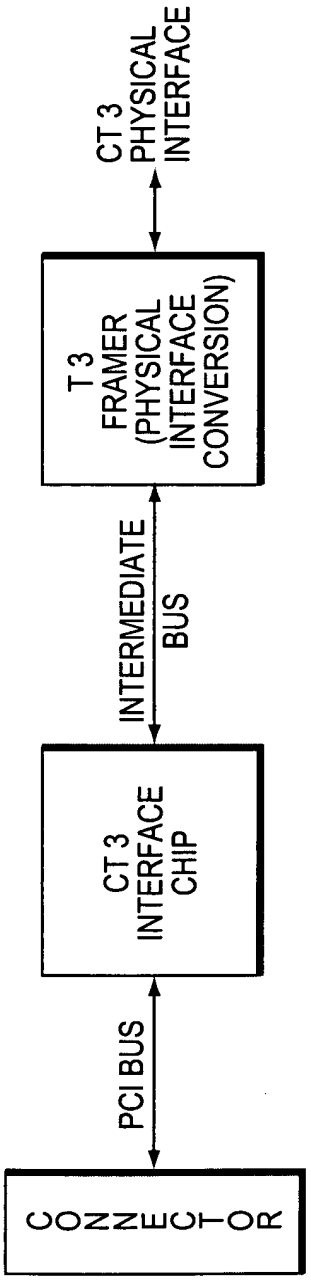


FIG. 3B

PORT ADAPTER DESIGN FOR A CHANNELIZED T3 LINE (CT 3) INTERFACE

OIPE JC48
DEC 29 2004
PATENT & TRADEMARK OFFICE

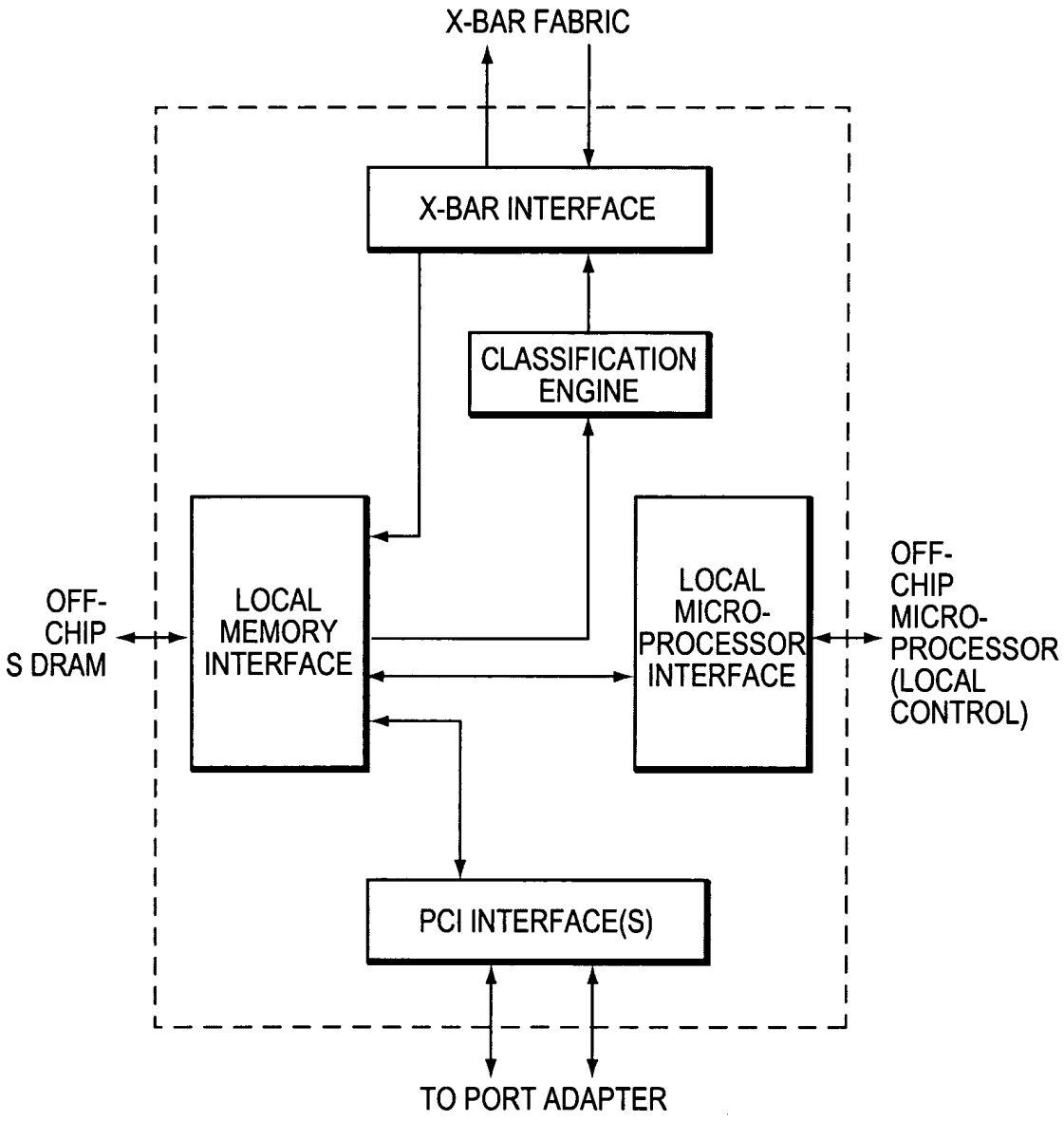


FIG. 4

OIPE
DEC 2 9 2004
PATENT & TRADEMARK OFFICE

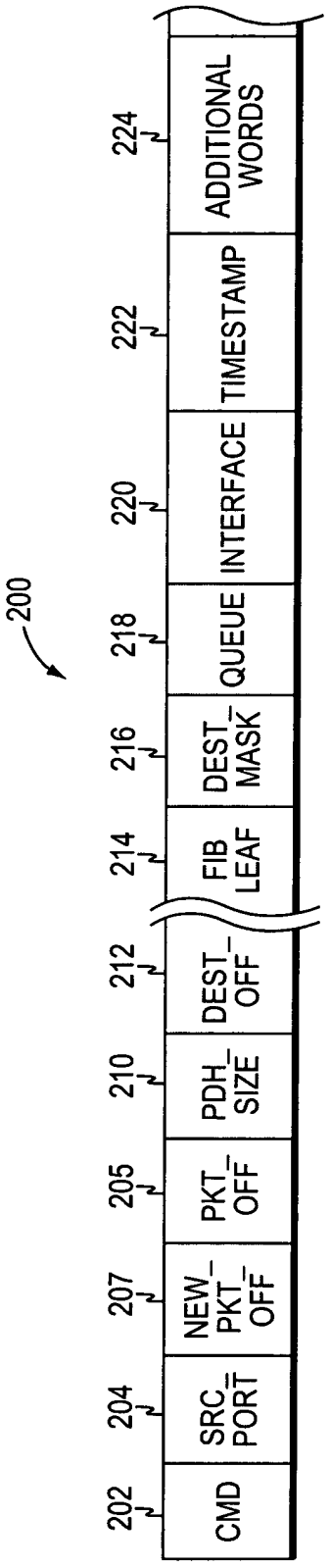


FIG. 5